

10/518,743

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	vary\$3 and (input test sequence\$1 same optimization algorithm)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/23 10:49
L4	94	monitor\$3 and (output\$1 same (faulty circuit\$1 or fault free circuit\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/23 10:44
L5	11	(identify\$3 or monitor\$3 or detect\$3 or verify\$3) and (digitized input sequence\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/23 10:45
L7	45711	mixed signal circuit\$1 or simulation circuit or computer simulation or analogee or analog circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/23 10:46
L10	18	I7 and (I4 or I5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/23 10:47
L12	1265	(fault free circuit\$1 or faulty circuit\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/23 10:48
L13	13	I10 and I12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/23 10:54
L16	52	(digitized same test sequence\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/23 10:48

EAST Search History

L22	65	input test sequence\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/23 11:42
L26	2	l7 and (l16 or l22)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/23 10:55
L28	5	l12 and (l16 or l22)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/23 11:27
L30	10078	714/724 or 714/741 or 714/740 or 714/799 or 714/819 or 714/48 or 714/47 or 703/13 or 703/21 or 714/36 or 714/37	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/23 11:39
L31	12941	test sequence\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/23 11:43
L32	2	l13 and l31	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/23 11:43